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Mail Stop Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Re:

U.S. Utility Patent Application

Appl. No. 09/844,296; Filed: April 30, 2001

Timing Recovery and Frequency Tracking System and Method

Inventors:

BUCHWALD et al.

Our Ref:

1875.0560002

Sir:

Transmitted herewith for appropriate action are the following documents:

- 1. Credit Card Payment Form (PTO-2038);
- 2. Fee Transmittal Form (PTO/SB/17);
- Second Supplemental Information Disclosure Statement; 3.
- 4. Ten (10) sheets of Form PTO-1449 listing ninety-two (92) documents;
- 5. One (1) copy each of sixty-seven (67) documents; and
- One (1) return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

Sterne, Kessler, Goldstein & Fox p.l.l.c.

Attorney for Applicants Registration No. 39,987

PEG/WWJ/mlb Enclosures

369260 1.DOC

Sterne, Kessler, Goldstein & Fox PLLC. : 1100 New York Avenue, NW : Washington, DC 20005 : 202.371.2600 f 202.371.2540 : www.skqf.com

Equivalent to Form PTO/SB/17 (12-04)

Approved for use through 07/31/2006. U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE Under the Paperwork Reduction Act of 1995 no persons are required to respond to a collection of information unless it displays a valid OMB control number Complete if Known Effective on 12/08/2004. Fees pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4818). Application Number 09/844,296 TRANSMI Filing Date April 30, 2001 For FY 2005 First Named Inventor BUCHWALD et al. Examiner Name Perilla, Jason M. Applicant claims small entity status. See 37 CFR 1.27 Art Unit 2634 TOTAL AMOUNT OF PAYMENT (\$) 180.00 Attorney Docket No. 1875.0560002 METHOD OF PAYMENT (check all that apply) Check X Credit Card Other (please identify): Money Order None 19-0036 Deposit Account Deposit Account Number: Deposit Account Name: Sterne, Kessler, Goldstein & Fox P.L.L.C. For the above-identified deposit account, the Director is hereby authorized to: (check all that apply) Charge fee(s) indicated below Charge fee(s) indicated below, except for the filing fee Charge any additional fee(s) or underpayments of fee(s) Credit any overpayments under 37 CFR 1.16 and 1.17 WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038. **FEE CALCULATION** 1. BASIC FILING, SEARCH, AND EXAMINATION FEES **FILING FEES** SEARCH FEES EXAMINATION FEES Small Entity Small Entity **Small Entity** Fees Paid (\$) **Application Type** Fee (\$) Fee (\$) Fee (\$) Fee (\$) Fee (\$) Fee (\$) Utility 300 200 150 500 250 100 Design 200 100 100 130 50 65 Plant 200 100 300 150 160 80 300 500 600 Reissue 150 250 300 Provisional 200 100 0 2. EXCESS CLAIM FEES **Small Entity** Fee (\$) Fee (\$) Fee Description 50 Each claim over 20 or, for Reissues, each claim over 20 and more than in the original patent 25 Each independent claim over 3 or, for Reissues, each independent claim more than in the original patent 200 100 360 180 Multiple dependent claims **Total Claims** Extra Claims **Multiple Dependent Claims** Fee Paid (\$) Fee (\$) \_ - 20 or HP Fee (\$) Fee Paid (\$) 34 HP = highest number of total claims paid for, if greater than 20 **Extra Claims** Fee (\$) Fee Paid (\$) Indep. Claims HP = highest number of independent claims paid for, if greater than 3 3. APPLICATION SIZE FEE If the specification and drawings exceed 100 sheets of paper, the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s). Number of each additional 50 or fraction thereof Fee Paid (\$) **Total Sheets Extra Sheets** (round up to a whole number) x - 100 = / 50 = 4. OTHER FEE(S) Fees Paid (\$) Non-English Specification, \$130 fee (no small entity discount) 180.00 Other: Information Disclosure Statement fee

SUBMITTED BY					
Signature	ato Examete	Registration No. (Attorney/Agent)	39,987	Telephone	(202) 371-2600
Name (Print/Type)	Patrick E. Garrett			Date 2	28/05

This collection of information is required by 37 CFR 1.136. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comment, on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Confirmation No.: 1034

BUCHWALD et al.

Art Unit: 2634

Appl. No.: 09/844,296

Examiner: Perilla, Jason M.

Filed: April 30, 2001

Atty. Docket: 1875.0560002

For:

**Timing Recovery and Phase Tracking System and Method** 

**Second Supplemental Information Disclosure Statement** 

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

Listed on accompanying Form PTO-1449 are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Where the publication date of a listed document does not provide a month of publication, the year of publication of the listed document is sufficiently earlier than the effective U.S. filing date and any foreign priority date so that the month of publication is not in issue. Applicants have listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

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This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Applicants have checked the appropriate boxes below.

- ☐ 1. Statement under 37 C.F.R. 1.704(d). Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of this information disclosure statement.
- 2. Filing under 37 C.F.R. § 1.97(b). This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits. No statement or fee is required.
- - a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than

three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).

- b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- ☐ 4. Filing under 37 C.F.R. § 1.97(d) This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee. Enclosed find our PTO-2038 Credit Card Payment Form in the amount of \$\_\_\_\_\_\_ in payment of the fee under 37 C.F.R. § 1.17(p); in addition:
  - a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than

three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).

- b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- 5. The document(s) was/were cited in a search report by a foreign patent office in a counterpart foreign application. Submission of an English language version of the search report that indicates the degree of relevance found by the foreign office is provided in satisfaction of the requirement for a concise explanation of relevance. 1138 OG 37, 38.
- 6. A concise explanation of the relevance of the non-English language document(s) appears below in accordance with 37 C.F.R. § 1.98(a)(3).
- ∑ 7. Copies of the documents listed in the sections entitled 'Foreign Patent
  Documents' and 'Other Documents' are enclosed. However, in accordance
  with 37 C.F.R. § 1.98(a)(2), copies of the U.S. patents and patent application
  publications cited on the attached Form PTO-1449 are not enclosed.
- 8. Copies of the documents were cited by or submitted to the Office in an IDS that complies with 37 C.F.R. § 1.98(a)-(c) in Application No.

filed	, which is relied upon for an earlier filing da	ate under 35
U.S.C. § 120.	Thus, copies of these documents are not attached.	37 C.F.R. §
1.98(d).		

9. It is expected that the examiner will review the prosecution and cited art in parent application no. \_\_\_\_\_\_ in accordance with MPEP 2001.06(b), and indicate in the next communication from the office that the art cited in the earlier prosecution history has been reviewed in connection with the present application.

It is respectfully requested that the Examiner initial and return a copy of the enclosed Form PTO-1449, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Patrick E. Garrett

Attorney for Applicants Registration No. 39,987

Date:

1100 New York Avenue, N.W. Washington, D.C. 20005-3934 (202) 371-2600

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<b>2.</b> (	44	ORM PTO-1449		ATTY. DOCKET NO. 1875.0560002	APPL 09/84	ICATION NO. 4,296	
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EXAMINER	T	T	U.S. PATE	ENT DOCUMENTS	T		
INITIAL	_	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1	5,396,224	03/1995	Dukes et al.			
•	AB1	5,550,546	08/1996	Noneman et al.			
	AC1	5,768,268	06/1998	Kline et al.			
	AD1	5,822,143	10/1998	Cloke et al.			
	AE1	5,881,107	03/1999	Termerinac et al.			
		F	OREIGN PA	ATENT DOCUMENTS			
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AF1	EP 1 006 697	06/2000	EP			N/A
	AG1	EP 1 139 619	10/2001	EP			N/A
	AH1	WO 01/29991	04/2001	wo			N/A
		OTHER DOCUMENTS	(Including	Author, Title, Date, Per	tinent Pages,	etc.)	
	AJ1	Conroy, C. et al., "An Solid-State Circuits, II	8-b 85-MS/s EEE, Vol. 28	Parallel Pipeline A/D Co , No. 4, April 1993, page	onverter in 1-µr s 447-454.	m CMOS," <i>IE</i>	
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	AK1	Dally, W. and Poulton Interconnects IV, Palo	, J., "Transm Alto, CA, 19	nitter Equalization for 4G 996, 10 pages.		Proceeding	
	AK1	Interconnects IV, Palo	Serial-Link	nitter Equalization for 4Gi 996, 10 pages. Transceiver Based on 8C al Solid-State Circuits Co	b/s Signalling,"	and D/A Co	s of Hot
		Ellersick, W. et al., "A 0.25µm CMOS," IEEE 430.	Serial-Link  Sinternation  SAD: A 12-G	996, 10 pages.  Transceiver Based on 80	b/s Signalling," GSample/s A/D onference, IEE	and D/A Co E, 2001, pag qualized Mul	s of Hot enverters in e 58-59 and
	AL1	Ellersick, W. et al., "A 0.25µm CMOS," IEEE 430.  Ellersick, W. et al., "G Symposium on VLSI	Serial-Link EInternation  AD: A 12-G Circuits Dige	Transceiver Based on 80 al Solid-State Circuits Co	b/s Signalling," GSample/s A/D onference, IEEI verter for an E 1999, pages 49	and D/A Co E, 2001, pag qualized Mul 9-52.	s of Hot  nverters in e 58-59 and  Iti-Level Link,
EXAMINER	AL1	Ellersick, W. et al., "A 0.25µm CMOS," IEEE 430.  Ellersick, W. et al., "G Symposium on VLSI Eklund, J-E. and Gus Random Chopper Sa	Serial-Link EInternation  AD: A 12-G Circuits Dige	Transceiver Based on 80 al Solid-State Circuits Co	b/s Signalling,"  SSample/s A/D  onference, IEEI  verter for an E 1999, pages 49  ation of Time-Ir  um on Circuits	and D/A Co E, 2001, pag qualized Mul 9-52.	s of Hot  nverters in e 58-59 and  Iti-Level Link,
	AL1 AM1	Ellersick, W. et al., "A 0.25µm CMOS," IEEE 430.  Ellersick, W. et al., "G Symposium on VLSI Eklund, J-E. and Gus Random Chopper Sa 28-31, 2000, pages II	Serial-Link  Serial-Link  Internation  AD: A 12-G  Circuits Dige  tafsson, F., ' mpling," IEE  I-447 thru III	Transceiver Based on 80 al Solid-State Circuits Co	b/s Signalling,"  GSample/s A/D  onference, IEEI  verter for an E 1999, pages 49  ation of Time-Ir  um on Circuits	qualized Mul 0-52. hterleaved Al and Systems	s of Hot  nverters in e 58-59 and  Iti-Level Link,  OC Using s, IEEE, May

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CI	ASS	SUB-CLASS	FILING DATE
INTIAL	AA2	5,949,820	09/1999	Shih et al.	, OL	A00_	30B-CLASS	TILINO DATE
	AB2	6,005,445	12/1999	Katakura				
ī.	AC2	6,009,534	12/1999	Chiu et al.				
	AD2	6,038,269	03/2000	Raghavan				
	AE2	6,134,268	10/2000	McCoy				
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	AF2	WO 01/54317	07/2001	wo				N/A
	AG2	WO 01/65788	09/2001	wo				N/A
	AH2	WO 01/84702	11/2001	wo			<u> </u>	N/A
	<del></del>	OTHER DOCUMENTS	(Including	Author, Title, Date, P	Pertinent Pa	ges,	etc.)	
	Al2	Fu, D. et al., "A Digita Converters," IEEE Jou 1904-1911.	l Background urnal of Solid	Calibration Techniqu -State Circuits, IEEE,	e for Time-lı Vol. 33, No.	nterle 12, [	eaved Analog December 19	g-to-Digital 998, pages
	AJ2	Guizani, M. and Al-Ali Measurement Techno	, A., "PC-Cor ology Confere	npatible Optical Data nce, IEEE, May 10-12	Acquisition ( 2, 1994, pag	Unit," es 10	Instrumenta 199-1102.	ation and
	AK2	Jenq, YC., "Digital S Estimation Algorithm Instrumentation and M	for Ultra High	-Speed Waveform Dig	gitizers Usin	g Inte	erleaving" <i>Tra</i>	ime Offset ansactions on
	AL2	Mason, R. and Taylor International Symposi						s," <i>IEEE</i>
	AM2	Niewczas, P. et al., "E Processing System," 6, December 2000, pa	IEEE Transa	ctions on Instrumenta	t Transducer	r Ope asure	erating with a	Digital Signal Vol. 49, No.
	AN2	Petraglia, A. and Mitra Interleaved Waveform Vol. 40, No. 5, Octobe	n Digitizer," <i>IE</i>	EEE Transactions on I	s Among A/I Instrumentat	O Cor tion a	nverters in a nd Measure	Time- ment, IEEE,
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Page 3 of 10 APPLICATION NO. ATTY, DOCKET NO. FORM PTO-1449 1 1875.0560002 09/844,296 **INVENTORS** SECOND SUPPLEMENTAL BUCHWALD et al. INFORMATION DISCLOSURE STATEMENT FILING DATE ART UNIT April 30, 2001 2634 **U.S. PATENT DOCUMENTS EXAMINER** SUB-CLASS | FILING DATE INITIAL DOCUMENT NUMBER DATE NAME CLASS AA3 12/2001 Wu 6,329,859 B1 03/2002 Chen AB3 6.359,486 B1 05/2002 Toda AC3 6,397,048 B1 06/2002 AD3 6,404,525 B1 Shimomoura et al. AE3 6,498,694 B1 12/2002 Shah **FOREIGN PATENT DOCUMENTS EXAMINER** SUB-CLASS | TRANSLATION INITIAL DATE COUNTRY CLASS DOCUMENT NUMBER WO 01/84724 11/2001 wo AF3 02/2002 wo N/A AG3 WO 02/13424 N/A 09/2002 wo WO 02/071616 AH3 OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) Sauer-Greff, W. et al., Maximum-Likelihood Sequence Estimation of Nonlinear Channels in High-Speed Optical Fiber Systems, April 6, 2001, Retrieved from the Internet at AI3 http://www.ftw.at/Dokumente/010406a.pdf, 29 pages. Williamson, R.C. et al., "Effects of Crosstalk in Demulitplexers for Photonic Analog-to-Digital Converters," Journal of Lightwave Technology, IEEE, Vol. 19, No. 2, February 2001, pages 230-AJ3 236. Yang, C-K., Design Techniques for High-Speed Chip-to-Chip Links, Retrieved from the Internet at AK3 http://web.doe.carleton.ca/courses/97578/topic5/Tutorial\_SerialLink.pdf, 31 pages. Yang, C-K. et al., "A Serial-Link Transceiver Based on 8-Gsamples/s A/D and D/A Converters in 0.25-µm CMOS," IEEE Journal of Solid-State Circuits, IEEE, Vol. 36, No. 11, November 2001, AL3 pages 1684-1692. Zuoxi, T., "Implementation of a Digital Multibeam Receiver Based on TMS320C80 for Laser AM3 Optoacoustic Remote Sensing," Proceedings of ICSP2000, IEEE, 2000, pages 2082-2084. Agazzi, O. and Lenosky, T., Algorithm to Postprocess Measured Data, IEEE 802.3ae Equalization AN3 Ad Hoc Group, January 10, 2001, 11 pages. DATE CONSIDERED **EXAMINER** 

**EXAMINER**: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance

and not considered. Include copy of this form with next communication to Applicant.

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		FORM PTO-1449		ATTY. DOCKET NO. 1875.0560002	APPL 09/84	ICATION NO. 4,296	
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	AA4	6,509,773 B2	01/2003	Buchwald et al.			
-	AB4	6,621,862 B1	09/2003	Dabell			
	AC4	6,791,388 B2	09/2004	Buchwald et al.			
	AD4	2002/0012152 A1	01/2002	Agazzi et al.			
	AE4	2002/0034222 A1	03/2002	Buchwald et al.			
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	AF4						Yes No
	AG4						Yes No
	AH4						Yes No
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	AJ4	Agazzi, O. et al., DSF Implementation, IEEE	P-Based Equ E 802.3ae, N	ualization for Optical Chan New Orleans, September 1	nels: Feasibili 2-14, 2000, 39	ty of a VLSI 9 pages.	
	AK4			ations on Multimode Optica vember 5, 2000, 29 pages		EEE 802.3ae	<b>;</b> -
	AL4	Agazzi, O. et al., Mea Equalization Ad Hoc,		of DMD-Challenged Fiber , 2001, 33 pages.	s at 3.125Gb/s	s, IEEE 802.3	3ae
	AM4	Agazzi, O. and Lenos November 24, 2000,		asurement of Non-Stationa	arity of 10 Gb/s	s Multimode	Fiber Links,"
	AN4	Agazzi, O., 10 Gb/s I pages.	PMD Using I	PAM-5 Modulation, IEEE 8	302.3, Dallas,	January 18-2	20, 2000, 19
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INITIAL	AA5	2002/0039395 A1	04/2002	Buchwald et al.	CLASS	30B-CLA33	FILING DATE	
-	AB5	2002/0044618 A1	04/2002	Buchwald et al.				
	AC5	2002/0080898 A1	06/2002	Agazzi et al.				
	AD5	2003/0086515 A1	05/2003	Trans et al.				
	AE5	2004/0212416 A1	10/2004	Buchwald et al.				
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	AI5	Agazzi, O. <i>et al.</i> , <i>10 G</i> March 6-10, 2000, 38		ing PAM-5 Trellis Coded Mo	odulation, IE	EEE 802.3, A	Alburquerque,	
	AJ5	Bhatt, V., Equalization	Ad Hoc Co	ncluding Report, IEEE P802	:.3ae Plena	ry, March 20	01, 12 pages.	
	AK5	Bingham, J.A.C., "Mul	ticarrier Mod s <i>Magazine</i> ,	dulation for Data Transmissi IEEE, Vol. 28, No. 5, pages	on: An Idea 5-8 and 11	Whose Time	e Has Come,"	
	AL5	Chiddix, J. et al., "AM on Selected Areas in (	Video on Fil Communicat	ber in CATV Systems: Need tions, IEEE, Vol. 8, No. 7, Se	l and Implei	mentation," <i>I.</i> 990, pages 1	<i>EEE Journal</i> 1229-1239.	
	AM5	Darcie, T., "Subcarrier Journal on Selected A 1248.	r Multiplexing reas in Com	g for Lightwave Networks ar nmunications, IEEE, Vol. 8, I	nd Video Di No. 7, Sept	stribution Sy ember 1990,	stems," <i>IEEE</i> pages 1240-	
	AN5	Fettweis, G., "High-Ra	ate Viterbi Pi ions, IEEE, V	rocessor: A Systolic Array S Vol. 8, No. 8, October 1990,	olution," <i>IE</i> pages 152	<i>EE Journal o</i> 0-1534.	n Selected	
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	AF6						Yes No
	AG6						Yes No
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	AJ6	1973, pages 268-278		gorithm," <i>Proceedings of</i> Speed Study Group, IEEE			
	AL6	Giaretta, G. and Lend	osky, T., <i>Ad</i> Plenary, Ma	laptive Equalization of DN arch 11, 2001, 10 pages.	MD Challenged I	Multimode Fi	iber at 1300
	AM6			nsiderations for Gigabit E m Integrated Circuits Col			
	AN6			of Fiber Responses at 5 ( loc, March 11, 2001, 18 p		Using 850nr	m VCSELs,
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	AG7						Yes No
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	AI7	Kanno, N. and Ito, K. QAM," <i>IEEE Journal</i> pages 1313-1319.	, "Fiber-Op on Selecte	otic Subcarrier Multiplexing V d Areas in Communications	/ideo Transpo , IEEE, Vol. 8	ort Employing , No. 7, Sept	g Multilevel tember 1990,
	AJ7	Kasper, B.L., "Equali Journal, American Te 1367-1388.	zation of M elephone a	lultimode Optical Fiber Systend Telegraph Company, Vol	ems," <i>The Bel</i> . 61, No. 7, S	ll System Te eptember 19	<i>chnical</i> 982, pages
	AK7	Kasturia, S. and Wint Cancellation," <i>IEEE</i> 3 pages 711-717.	ters, J., "Te lournal on	echniques for High-Speed Im Selected Areas in Communi	nplementation cations, IEEE	of Nonlinea , Vol. 9, No.	r 5, June 1991,
	AL7	Lenosky, T., <i>A Unifie</i> Equalization Ad Hoc	d Method o Meeting, T	of Calculating PMD-induced ampa, Florida, November 5,	Pulse Broade , 2000, 8 page	ening, IEEE 8 es.	302.3ae
	AM7			ive Gb/s Multimode DMD at n, March 11, 2001, 13 pages		'-Time Data a	and Equalizer
	AN7			nts of DMD-Challenged Fiber Group, January 10, 2001, 21		nd 2Gb/s Da	ata Rate, IEEE
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	AF8						No Yes
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	AJ8	Liu, M-K. and Modest Subcarrier Multiplexin 7, pages 1239-1246.	ou, P., "Mu g Transmis	Itilevel Signaling and Pulse sion," <i>IEEE Journal of Ligh</i>	Shaping for Stwave Technol	Spectrum Effology, IEEE,	ficiency in Vol. 12, No.
	AK8			Multiplexed Coherent Light in Communications, IEEE,			
	AL8	Olshansky, R. et al., ' Journal of Lightwave	Subcarrier Technology	Multiplexed Lightwave Syst ∕, IEEE, Vol. 7, No. 9, Septe	tems for Broa ember 1989, p	dband Distri pages 1329-	bution," <i>IEEE</i> 1341.
	AM8	Otte, S. and Rosenkr High Speed Optical T Networks, IEEE, 1999	ransmissio	Decision Feedback Equalizen Systems," <i>International C</i>	zer for Disper onference on	sion Compe <i>Transparen</i>	nsation in t Optical
	AN8	Parhi, K. et al., Parall IEEE 802.3ae Task F		ntation of the DSP Function n 2000, 12 pages.	ns of the PAM	'-5 10Gb/s T	ransceiver,
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	AG9						Yes No
	АН9						Yes No
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	AJ9	Personick, S.D., "Bas Systems," <i>Bell Syster</i> No. 7, September 197	eband Line m Technica 73, pages 1	earity and Equalization in F I Journal, American Teleph 175-1194.	iber Optic Digi none and Tele	tal Commun graph Compa	ication any, Vol. 52,
	AK9	Technical Journal, An 1973, pages 843-874	nerican Tel	ephone and Telegraph Co	mpany, Vol. 52	2, No. 6, July	/-August
	AL9	Conference, IEEE, At	lanta, Geor	gn for Digital Optic System gia, November 26-28, 197	3, Vol. 1, page	es 8E-1 - 8E-	-4.
	AM9	Vorenkamp, P. <i>et al.</i> , 2000, 13 pages.	Analog Inte	erface for 10-Gb/s Etherne	t, IEEE 802.3a	ae Task Forc	e, March
	AN9	Winters, J. and Gitlin, Systems," <i>IEEE Tran</i> 1439-1453.	R., "Electr sactions or	ical Signal Processing Ted Communications, IEEE, \	chniques in Loi Vol. 38, No. 9,	ng-Haul Fibe September 1	r-Optic 1990, pages
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	Al10	Winters, J. et al., "Red Systems," <i>IEEE Com</i>	ducing the E munications	Effects of Transmission Imp s <i>Magazine</i> , IEEE, June 199	airments in D 93, pages 68-	Digital Fiber 0 -76.	Optic
	AJ10	Progress Report on E Equalization, January	qualization 12, 2001, 1	of Multimode Fibers, IEEE 16 pages.	802.3ae Ad I	Hoc Group o	n
	AK10	Alderrou, D. et al., XA	AUI/XGXS F	Proposal, IEEE 802.3ae Tas	sk Force, May	/ 23-25, 2000	), 28 pages.
	AL10	Winters, J.H. and Gitl Systems," <i>IEEE Intern</i> pages 0397-0403.	in, R., "Elec	ctrical Signal Processing Te Inference on Communication	chniques in L ns, IEEE, Vol	ong-Haul, F . 1, April 16-	iber-Optic 19, 1990,
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